

ECR#: P20

Tracker #: None

Status: Ratified

Title: Change Pin Definitions to Accommodate AC '97 Component Specification (V2.0)

Release Date: September 1998

Impact, Medium: Motherboard design change

Spec Version: NLX Motherboard Specification 1.2

Summary:

Make text changes for pins X9, X10, X11, and Y8 in Table B.1: Signals, NLX Riser with Supplemental Connector.

Background:

The NLX Motherboard Specification V1.2 supported only one AC '97 device in the riser. To accommodate the Audio Codec '97 Component Specification V2.0, three reserved pins in the 2x13 supplemental connector need to be redefined to support the new AC '97 specification. These three pins now carry AC '97 input signals to the motherboard. The one pin that previously supported an AC '97 device has its signal name modified slightly to indicate that it now carries input signal 0 of 4.

Changes Current Specification As Shown:

In Table B.1 on pages 66 and 67, make text changes for pins X9, X10, X11, and Y8 as indicated below. Substitute the **bold** text below for the existing text for those pins in the table.

Table B.1: Signals, NLX Riser with Supplemental Connector

Pin	Signal Name	Type	I/O*	Description	Signal Type
X9	AC_SD_IN1	AC'97	I	Serial, time division, multiplexed, AC'97 input stream to the motherboard from the codec on the riser (output from codec).	TTL
X10	AC_SD_IN2	AC'97	I	Serial, time division, multiplexed, AC'97 input stream to the motherboard from the codec on the riser (output from codec).	TTL
X11	AC_SD_IN3	AC'97	I	Serial, time division, multiplexed, AC'97 input stream to the motherboard from the codec on the riser (output from codec).	TTL
Y8	AC_SD_IN0	AC'97	I	Serial, time division, multiplexed, AC'97 input stream to the motherboard from the codec on the riser (output from codec).	TTL